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| S1    | 383     | "coarse delay"           | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:12 |
| S2    | 586     | "fine delay"             | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:12 |
| S3    | 802593  | calculat??? or comput??? | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:12 |
| S4    | 1       | S1 with S2 with S3       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:13 |
| S5    | 2040345 | function                 | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:13 |
| S6    | 7       | S1 with S2 with S5       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:18 |
| S7    | 2       | "6417706".pn.            | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 15:19 |
| S8    | 2       | "6491634".pn.            | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 16:04 |
| S9    | 242     | S1 with S2               | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 16:04 |
| S10   | 1750    | predistortion            | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/03/01 16:04 |
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| S16 | 18     | S1 same S2 same S14 | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:18 |
| S17 | 540614 | delay               | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:28 |
| S18 | 2063   | feedback            | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:28 |
| S19 | 329359 | S12 or S18          | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:00 |
| S20 | 21230  | S17 same S19        | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:29 |
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| S22 | 609    | 375/296.ccls.       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:34 |
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| S24 | 594073 | amplifier           | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:34 |

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| S26 | 52174 | digital adj analog | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:35 |
| S27 | 39    | S25 and S26        | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:39 |
| S28 | 12617 | S12 with S17       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:39 |
| S29 | 28    | S22 and S28        | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:47 |
| S30 | 3913  | oversampling       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:47 |
| S31 | 242   | S1 with S2         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:50 |
| S32 | 2068  | oversampled        | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:54 |
| S33 | 4150  | S30 or S31         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:58 |
| S34 | 158   | S19 with S33       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:50 |
| S35 | 10    | S31 and S34        | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:50 |
| S36 | 4795  | S30 or S32         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:59 |

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| S40 | 271     | S1 same S2          | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:58 |
| S41 | 242     | S33 and S40         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:58 |
| S42 | 5       | S36 and S40         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 16:59 |
| S43 | 98      | S9 and S19          | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:08 |
| S44 | 2172494 | input               | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:08 |
| S45 | 298922  | sampling or sampled | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:08 |
| S46 | 17181   | S44 near2 S45       | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:09 |
| S47 | 27      | S40 and S46         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:09 |
| S48 | 17      | S13 and S46         | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/03/01 17:14 |

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**JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard1 **A low-power half-delay-line fast skew-compensation circuit***Yi-Ming Wang; Jinn-Shyan Wang;*

Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 6 , June 2004

Pages:906 - 918

[\[Abstract\]](#)   [\[PDF Full-Text \(1120 KB\)\]](#)   **IEEE JNL**2 **Development of a real time digital high frequency annular array ultrasound imaging system***Pei-Jie Cao; Chang-Hong Hu; Shung, K.K.;*

Ultrasonics, 2003 IEEE Symposium on , Volume: 2 , 5-8 Oct. 2003

Pages:1867 - 1870 Vol.2

[\[Abstract\]](#)   [\[PDF Full-Text \(303 KB\)\]](#)   **IEEE CNF**3 **A 667-Mb/s operating digital DLL architecture for 512-Mb DDR SDRAM***Hamamoto, T.; Furutani, K.; Kubo, T.; Kawasaki, S.; Iga, H.; Kono, T.; Konishi, Y.; Yoshihara, T.;*

Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 1 , Jan. 2004

Pages:194 - 206

[\[Abstract\]](#)   [\[PDF Full-Text \(1288 KB\)\]](#)   **IEEE JNL**4 **PulsON second generation timing chip: enabling UWB through precise timing***Kelly, D.; Reinhardt, S.; Stanley, R.; Einhorn, M.;*

Ultra Wideband Systems and Technologies, 2002. Digest of Papers. 2002 IEEE Conference on , 21-23 May 2002

Pages:117 - 121

[\[Abstract\]](#)   [\[PDF Full-Text \(654 KB\)\]](#)   **IEEE CNF**